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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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OCT 11 2002  
TECHNOLOGY CENTER 2800

In re application of:

NAKAGAWA, et al.

Art Unit: 2818

Serial No.: 09/810,499

Examiner: C. YOHA

Filed: March 19, 2001

Docket No.: 100353-00049

For: SEMICONDUCTOR MEMORY DEVICE WITH SHORTER SIGNAL LINES

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
Washington, D.C. 20231

Date: October 10, 2002

Sir:

In response to the Restriction Requirement dated September 10, 2002, Applicants hereby elect, without traverse, for further prosecution the invention of Group I, claims 1-9, drawn to memory array having plurality, sense amplifier, switches and their interconnection. Applicants reserve the right to file one or more divisional application(s) directed to the non-elected claims.

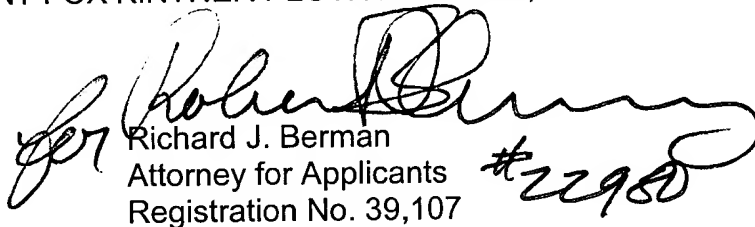
Accordingly, an early examination and an early action on the merits are respectfully requested.

In the event this paper is not considered to be timely filed, Applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2300. The Commissioner is hereby authorized to charge any fee deficiency or credit any overpayment associated with this

communication to Deposit Account No. 01-2300, referencing Attorney Docket No.  
100353-00049.

Respectfully submitted,

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